



Driving the Future of Software Defined Vehicles with industry open standards

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RISC-V - A Global Organization



- Membership
 - $\frac{1}{3}$ Asia
 - $\frac{1}{3}$ Americas
 - $\frac{1}{3}$ Europe
- Contribution from all geographies to Special Interest Groups and Technical Working Groups
- Enabling worldwide RISC-V Innovation
- Solving local problems with access to a global market

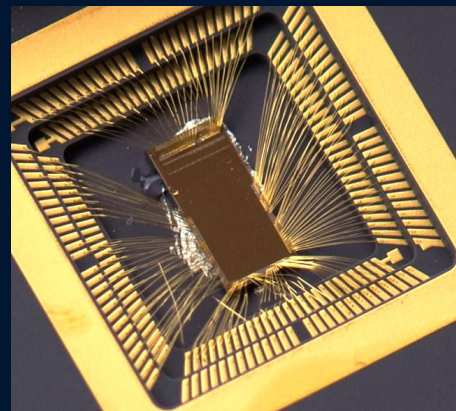
Global standards are a catalyst for innovation



Standards have been critical to technology innovation, adoption, and growth for decades



Standards create access to opportunities and spur growth for a wide range of stakeholders



RISC-V is a standards-defined Instruction Set Architecture developed by a global community

RISC-V is now an ISO/IEC JTC1 PAS Submitter



JTC1



INFORMATION TECHNOLOGY STANDARDS

Ballot passed in September 2025

RISC-V is now an ISO/IEC JTC1 PAS Submitter!



Ballot passed in September 2025

- RISC-V International (as a group of members) operates in line with the ISO/IEC JTC1 directives
- Policies and guidelines, our spec development process, our tech committees and voting procedures are fully aligned to JTC1
- RISC-V International is an entity eligible to submit specifications to become ISO/IEC JTC1 published standards
- We will now work to make the RISC-V ISA manuals become ISO/IEC JTC1 standards

The technical foundation for lasting success

Countries

- Tech sovereignty
- Accelerate local innovation and talent
- Incubate technology ecosystem from research to industry
- Access worldwide market

Multinationals

- Control strategic roadmap
- New opportunities for innovation and influence
- Growth business models
- Avoid vendor lock-in

Researchers

- Collaborative ISA thought leadership
- Enables innovative research
- Access global RISC-V research network
- Tech transfer addressing real world applications

Startups

- Supercharge hardware and software co-design
- Accelerate strategic roadmap in greenfield applications
- Collaboration partners

Priority Industry Verticals & Horizontals



Automotive



Data Center



Embedded / IoT



Consumer



HPC



Industrial



Space / Aerospace

Verticals



AI



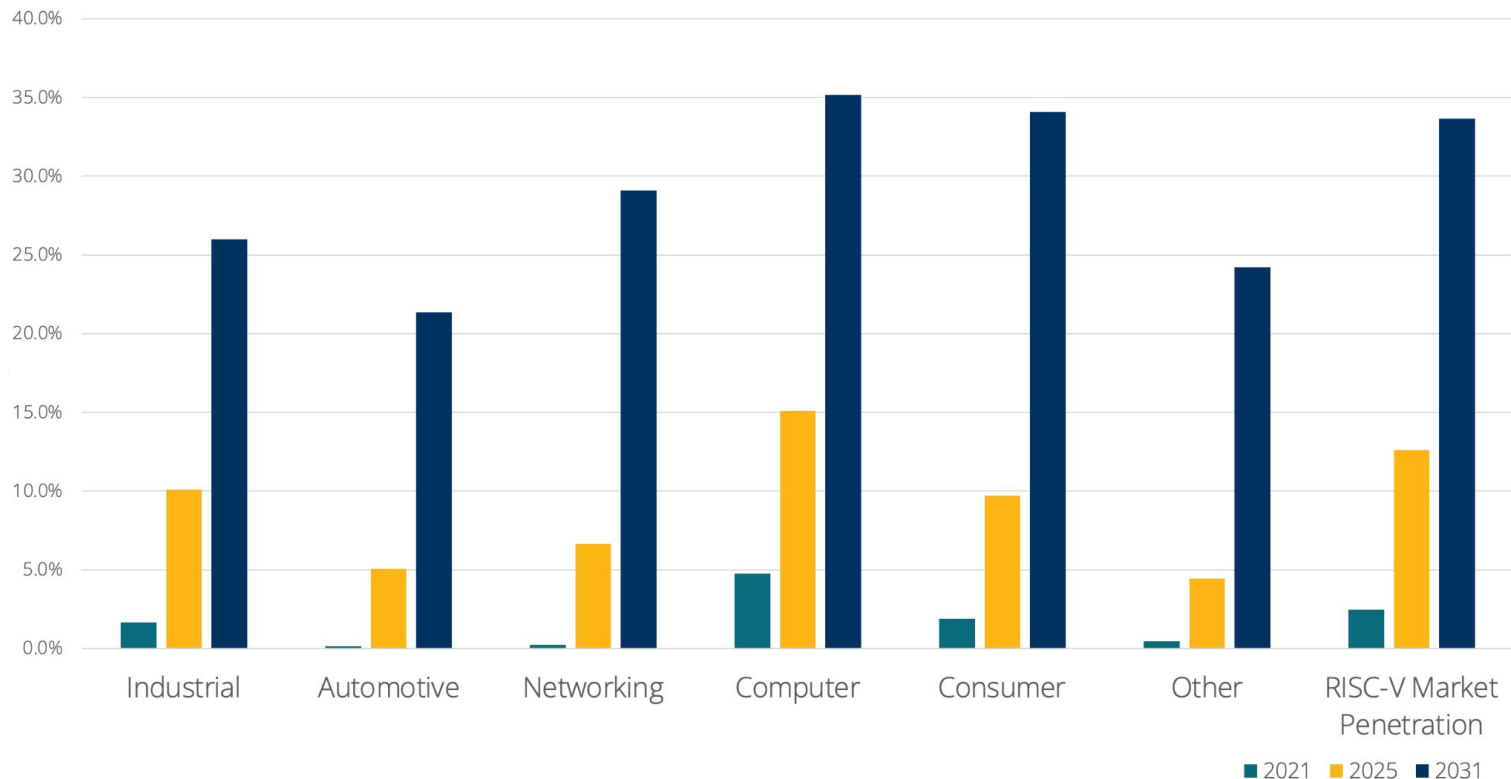
Security

Horizontals

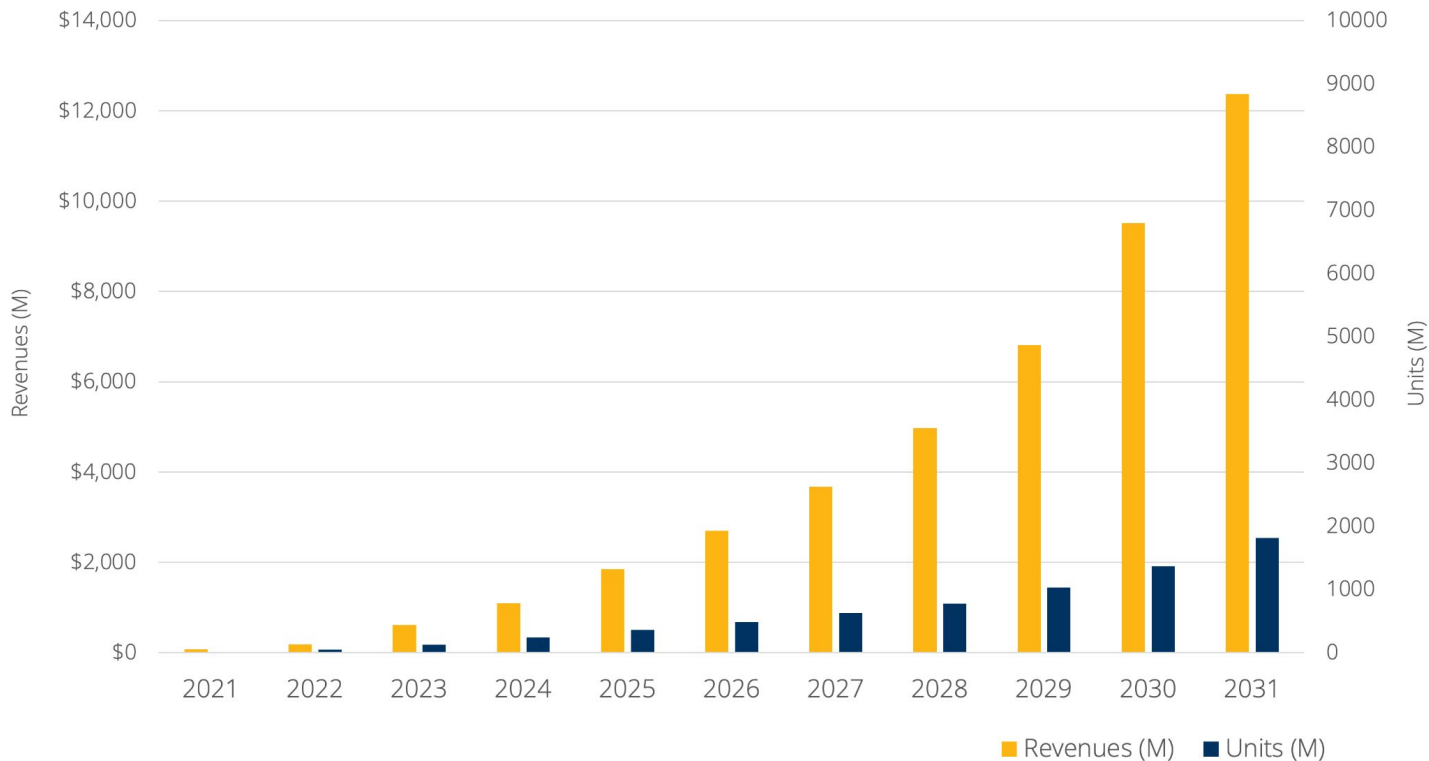
From 2.5% to 33.7% in 10 years!



RISC-V Market Penetration for Revenues by Major Category



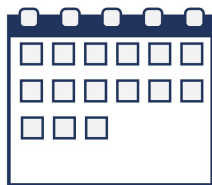
RISC-V Growth in the Automotive Market



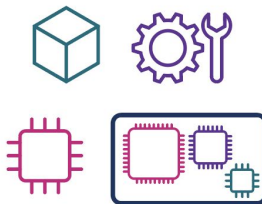
Gain control of your roadmap and supply chain



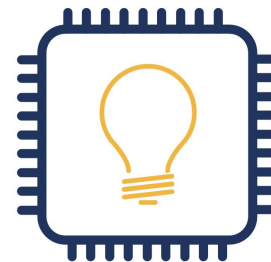
RISC-V is a global standard



An ecosystem that can be counted on for long term support



Wide breadth of choice enables easier multi-sourcing, reduces risk and vendor lock-in

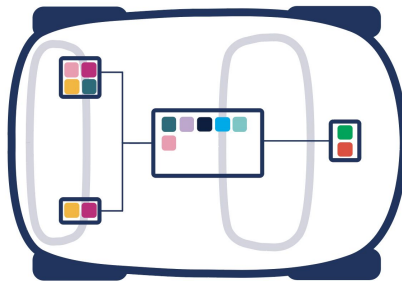


Create more innovative, differentiated and compelling automotive compute solutions



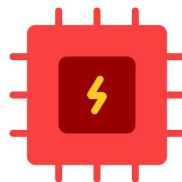
Source from a broad ecosystem and develop your own IP

RISC-V scales across the entire vehicle



ECU

32-bit MCU
Fast real time capabilities
Physical Memory Protection
Security



Zonal Controller

64-bit embedded CPU
AI-enabled with vector or matrix
Physical Memory Protection
Security



Central Compute

64-bit server grade CPU
AI-enabled Vector and Matrix
Supervisor-domain isolation
and Security
Hypervisor support for mixed
criticality use cases

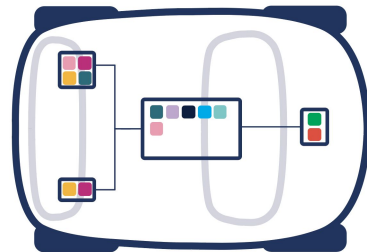
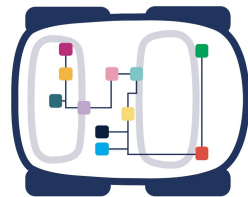
RISC-V scales across the whole vehicle



RISC-V enables the perfect balance of processing for every application

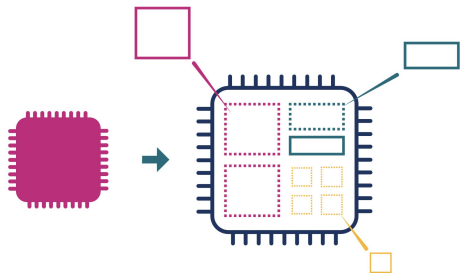


A single ISA enables all applications across the vehicle, and reuse of expertise and tools

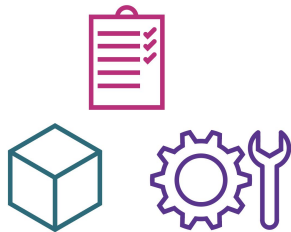


Software components easily deployed across a range of vehicle architectures, models and specifications

Supported by the capable RISC-V ecosystem



From sourcing SoCs, to
developing your own
silicon



Hardware technologies
from cores and IP to
tools and verification

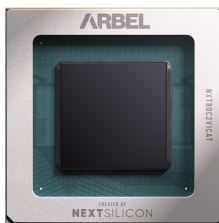
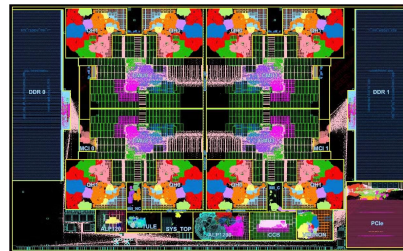
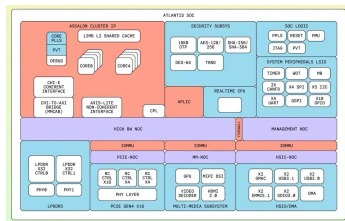


Software components
and development tools



"Many Eyes" model of
open development
benefits safety and
security

RISC-V Application Processors in 2026



Automotive Special Interest Group



- First RISC-V Automotive Conference in Germany
- Automotive MCU standard in progress
- FuSa Standards in progress
- Established growing ecosystem of recognised vendors
- RISC-V is already in production vehicles



RISC-V Automotive SIG



Advocate for auto in
Technical Groups and SIGs



Maintain a
“gap analysis”



Interface with
industry initiatives



Investigate AI for
automotive applications



Development of RISC-V
auto technologies at the
ISA level



Publish application notes
and white papers to help
developers worldwide



A rich ecosystem of **Software**

High performance **Silicon**

Versatile and extendable **ISA specs**

**Let's drive automotive
forward on RISC-V
together!**